

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E.

PATENT DATE

SCANNED PD² O.A. LR

APPLICATION NO. 09/717143	CONT/PRIOR F	CLASS 433 257	SUBCLASS 347	ART UNIT 2812 2812	EXAMINER 3333 S.
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APPLICANTS

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TITLE

Insulated gate type semiconductor device and method for fabricating same

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____		ISSUE FEE Amount Due Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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